

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	inductor adj free adj circuitry	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/08 07:55
L2	942	modular adj circuit\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/08 07:56
L3	182	modular adj circuit\$1.ti.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/08 07:56
L4	0	3 and(series with capacitor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/08 07:57
L5	11	3 and(capacitor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/08 07:57
L6	47	2 and(series with capacitor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/08 08:05
L7	8	US-4686619-\$.DID. OR US-5532575-\$.DID. OR US-6072691-\$.DID. OR US-6081095-\$.DID.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/08 08:23
L8	0	active adj balancing adj module	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/08 09:07
L9	1	"5710504".PN.	USPAT; USOCR	OR	OFF	2005/04/08 08:25

L10	1	"5648713".PN.	USPAT; USOCR	OR	OFF	2005/04/08 08:25
L11	1	"5504415".PN.	USPAT; USOCR	OR	OFF	2005/04/08 08:26
L12	1	"5867363".PN.	USPAT; USOCR	OR	OFF	2005/04/08 08:52
L13	1	"5604426".PN.	USPAT; USOCR	OR	OFF	2005/04/08 08:53
L14	1	"5545933".PN.	USPAT; USOCR	OR	OFF	2005/04/08 08:53
L15	1	"5402056".PN.	USPAT; USOCR	OR	OFF	2005/04/08 08:54
L16	1	"5260855".PN.	USPAT; USOCR	OR	OFF	2005/04/08 08:54
L17	1	"4975796".PN.	USPAT; USOCR	OR	OFF	2005/04/08 08:54
L18	1	"4922161".PN.	USPAT; USOCR	OR	OFF	2005/04/08 08:55
L19	1	"4757424".PN.	USPAT; USOCR	OR	OFF	2005/04/08 08:55
L20	1	"4731705".PN.	USPAT; USOCR	OR	OFF	2005/04/08 08:56
L21	1	"4725926".PN.	USPAT; USOCR	OR	OFF	2005/04/08 08:56
L22	154	balancing adj module	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/08 09:18
L23	7	balancing adj module with voltage	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/08 09:07
L24	0	361/325 and(balanc\$3 adj voltage)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/08 09:18
L25	0	361/326 and(balanc\$3 adj voltage)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/08 09:19

L26	1	361/326 and(balanc\$3 with voltage)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/08 09:19
L27	2	361/327 and(balanc\$3 with voltage)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/08 09:19
L28	3	361/324 and(balanc\$3 with voltage)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/08 09:19
L29	1	361/315 and(balanc\$3 with voltage)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/08 09:20
L30	2	"5684680".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/08 09:21
L31	2	"5365421".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/08 09:23
L32	1673	"363"/\$.ccls. and(balanc\$3 with voltage)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/08 10:16
L33	701	32 and(series with capacitor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/08 09:48

L34	0	active adj balancing adj modular adj circuits.ti.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/08 09:43
L35	0	balancing adj modular adj circuits. ti.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/08 09:43
L36	182	modular adj circuits.ti.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/08 09:43
L37	11	36 and(capacitor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/08 09:43
L38	159	anthony adj frank.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/08 09:44
L39	0	Doljack adj anthony adj frank.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/08 09:45
L40	5	schultz adj neal.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/08 09:45
L41	2	"20030099123"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/08 09:48

L42	2011	"361"/\$.ccls. and(balanc\$3 with voltage)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/08 10:16
L43	527	42 and(series with capacitor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/08 10:17
L44	1	43 and(circuitry)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/08 10:18
L45	220	43 and(circuitry)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/08 10:18
S1	124022	series with capacitor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/07 15:18
S2	15275	S1 and(control\$3 adj voltage)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/07 15:19
S3	1402	S2 and(module)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/07 15:19
S4	974	S3 and(circuitry)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/07 15:20

S5	26	S4 and(third adj terminal)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/08 07:55
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